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Examiner Initials* Cite	Number - Kind Code ² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentise or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
КT		US-6507625	01-14-2003	Iwamastsu et al.		
KT		US-6240142	05-29-2001	Kaufman et al.		
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FOREIGN PATENT DOCUMENTS						
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	NON PATENT LITERATURE DOCUMENTS					
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КT		"A VLSI Architecture for a High-Speed All-Digital Quadrature Modulator and Demodulator fo rDigital Radio Applications, H. Samueli, et al., IEEE Journal, vol., 8, No. 8, Oct. 1990, pges 1512-1519				
KT		"A Digital Quadrature Modulator With On-Chip D/A Converter", J. Vankka, IEEE Journal of Solid-state Circuits, Vol, 38, No. 10, Oct. 2003, pgs 1635-1642				
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